


**PATENT NUMBER**

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| OLIVER<br>CORNED <u>102</u> a.s. <u>Am</u> | PATENT DATE |
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**NTS** Makoto Kakegawa

Logical circuit designing device, logical circuit designing method,  
storage medium and program

PTO-2040  
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|   |                                      |            |           |  |                      |
|---|--------------------------------------|------------|-----------|--|----------------------|
|  <b>TERMINAL<br/>DISCLAIMER</b>  | <b>DRAWINGS</b>                      |            |           | <b>CLAIMS ALLOWED</b>                                |                      |
|   | Sheets Drwg                          | Figs. Drwg | Print Fig | Total Claims   | Print Claim for O.G. |
| <input type="checkbox"/> The term of this patent subsequent to _____ (date) has been disclaimed.  | _____<br>(Assistant Examiner) (Date) |            |           | <b>NOTICE OF ALLOWANCE MAILED</b>                    |                      |
| <input type="checkbox"/> The term of this patent shall not extend beyond the expiration date of U.S. Patent. No. _____  |                                      |            |           | <b>ISSUE FEE</b><br>Amount Due _____ Date Paid _____ |                      |
| <input type="checkbox"/> The terminal _____ months of this patent have been disclaimed.   | _____<br>(Primary Examiner) (Date)   |            |           |  |                      |
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